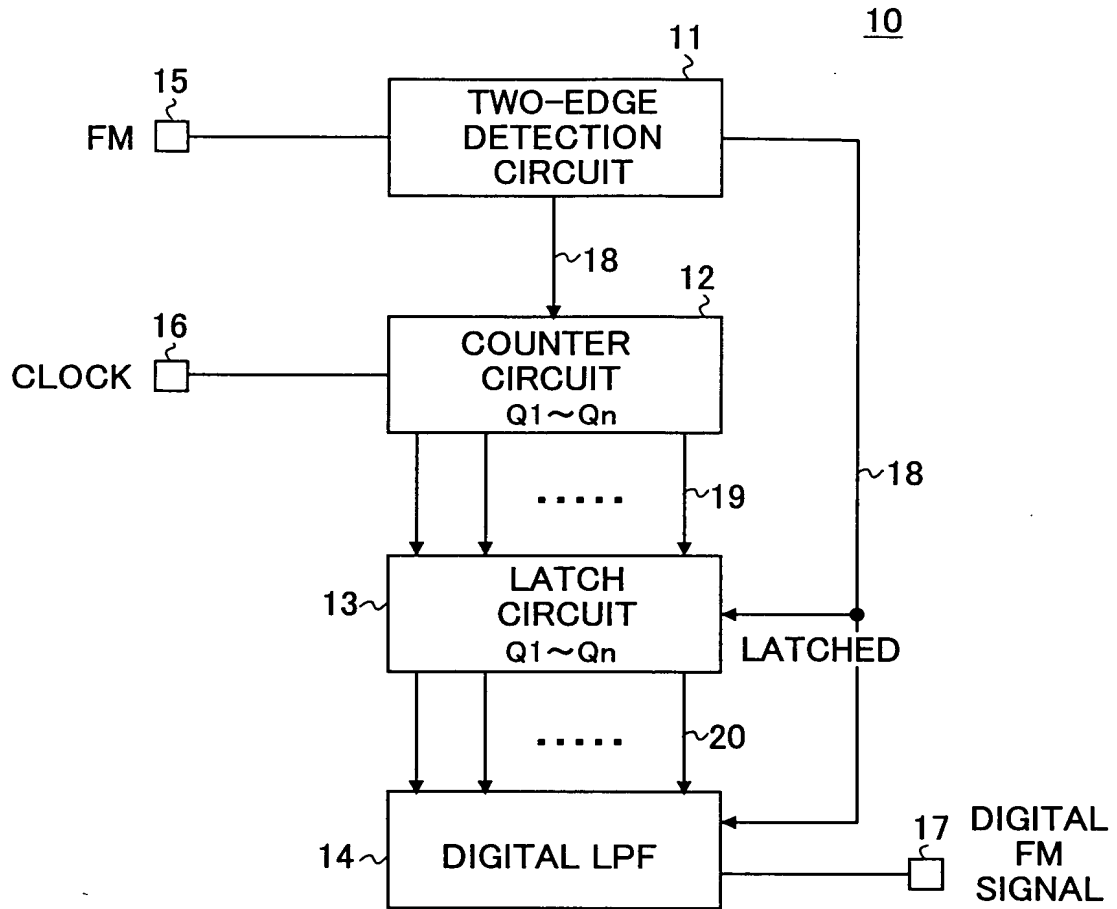


FIG.1 PRIOR ART



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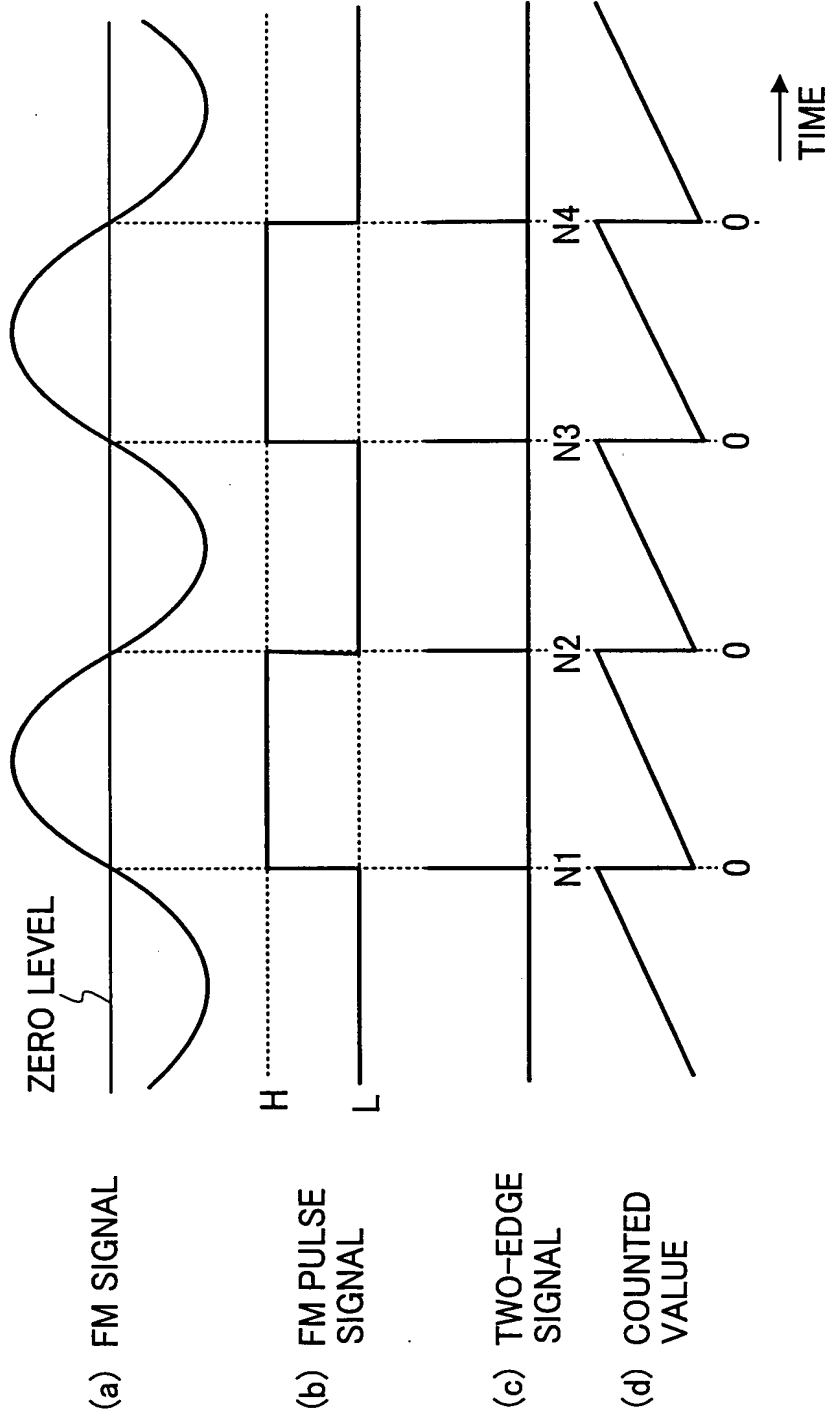
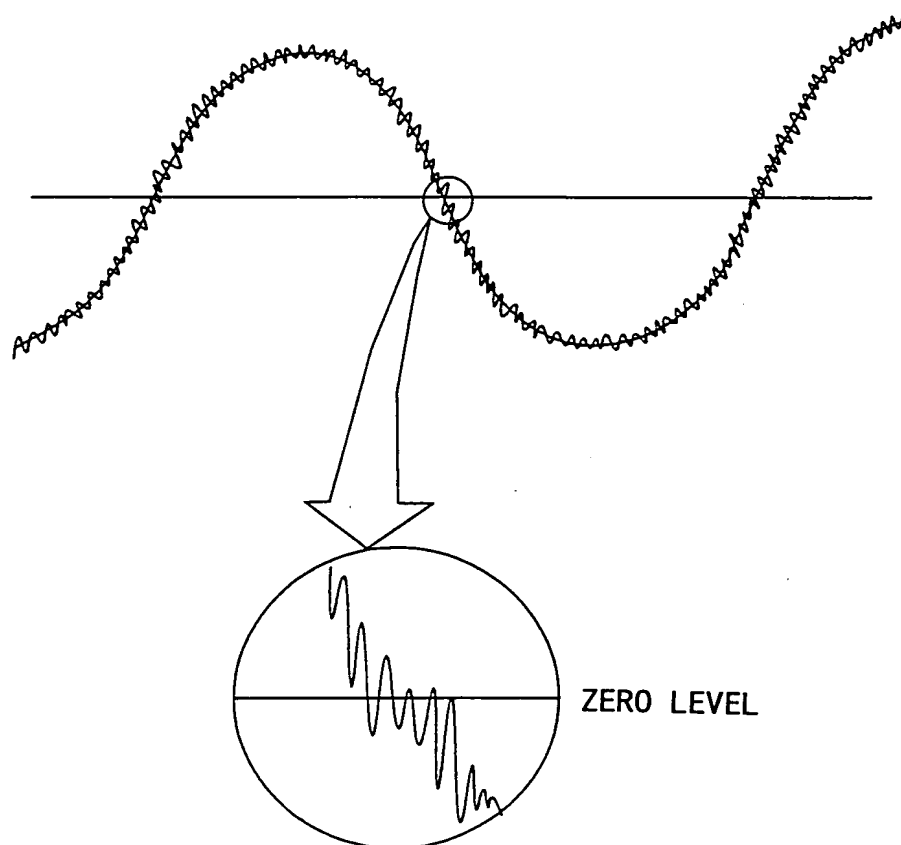
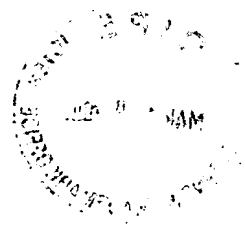


FIG.2
PRIOR
ART

FIG. 3 PRIOR ART



09740666.034901
TESTED 99907260



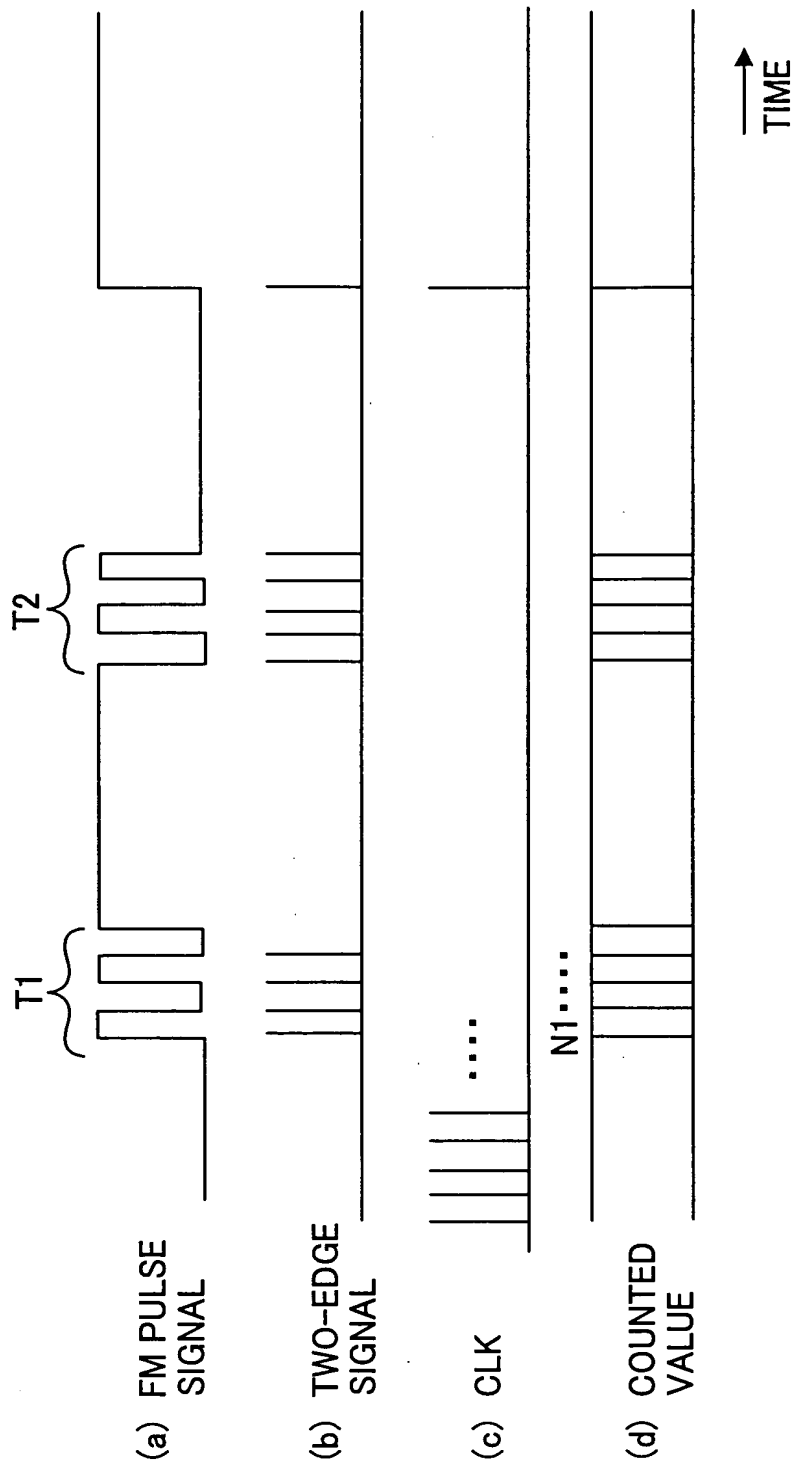


FIG.4
PRIOR
ART

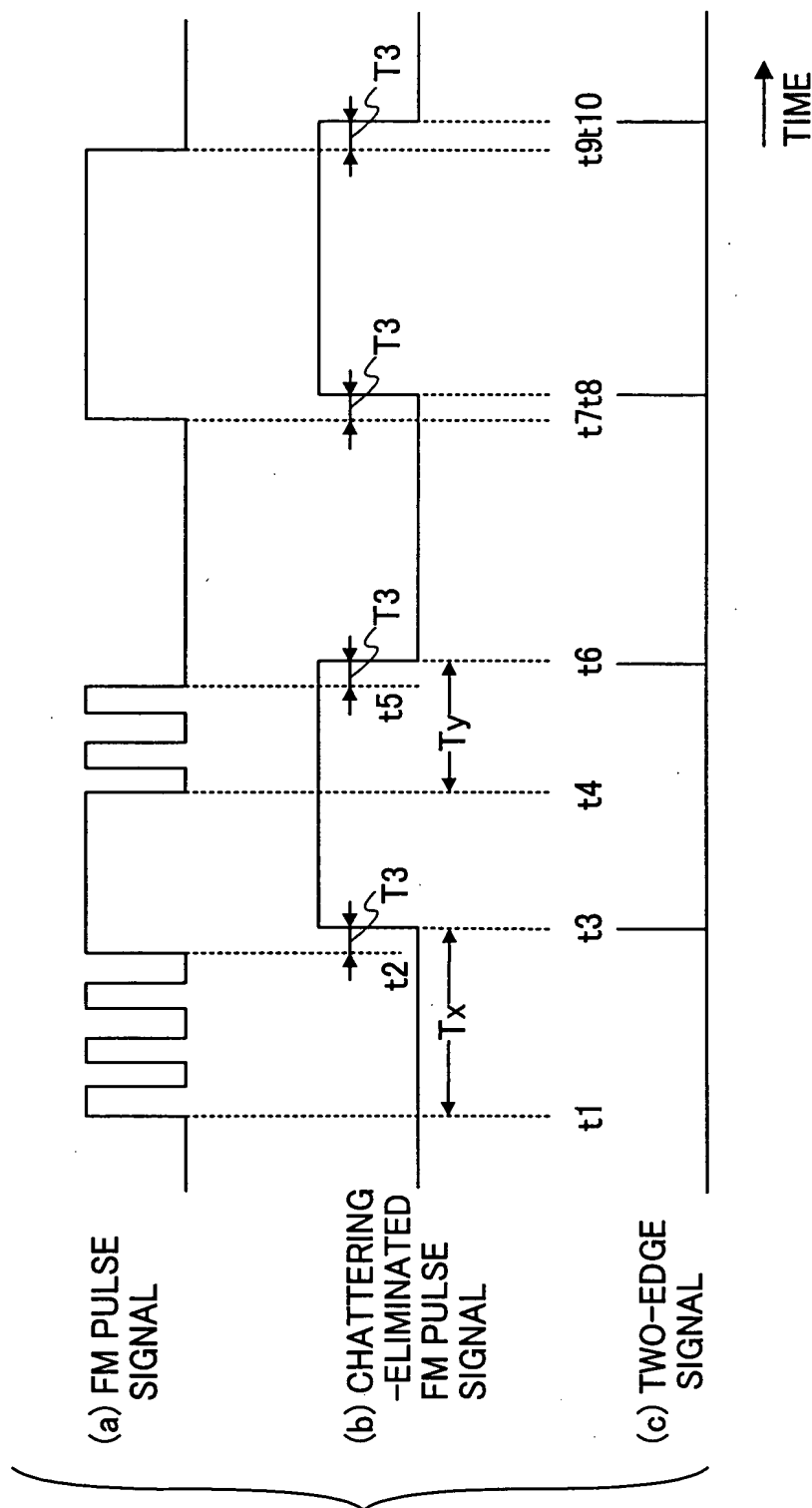


FIG.5
PRIOR
ART

FIG.6

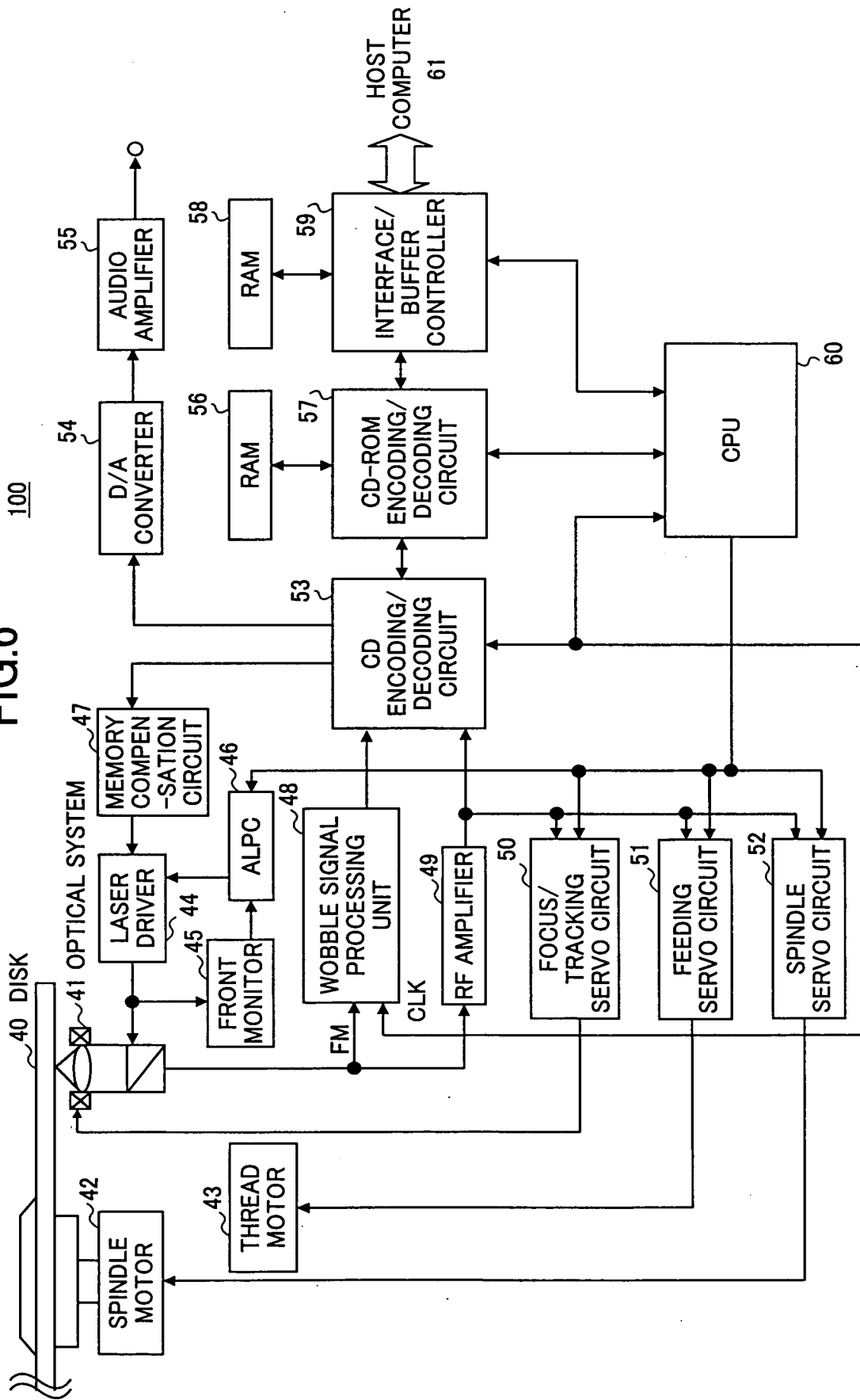
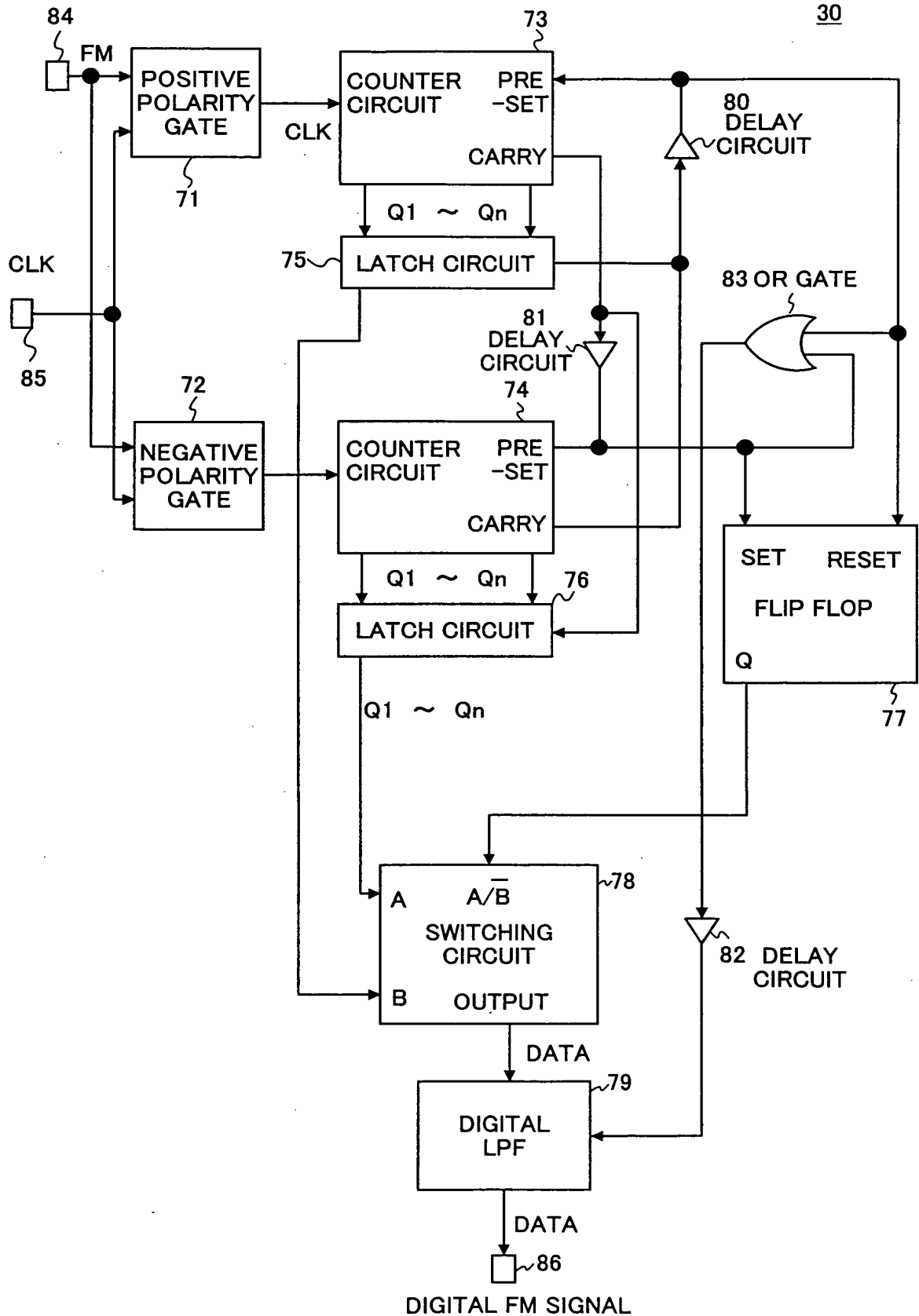


FIG. 7



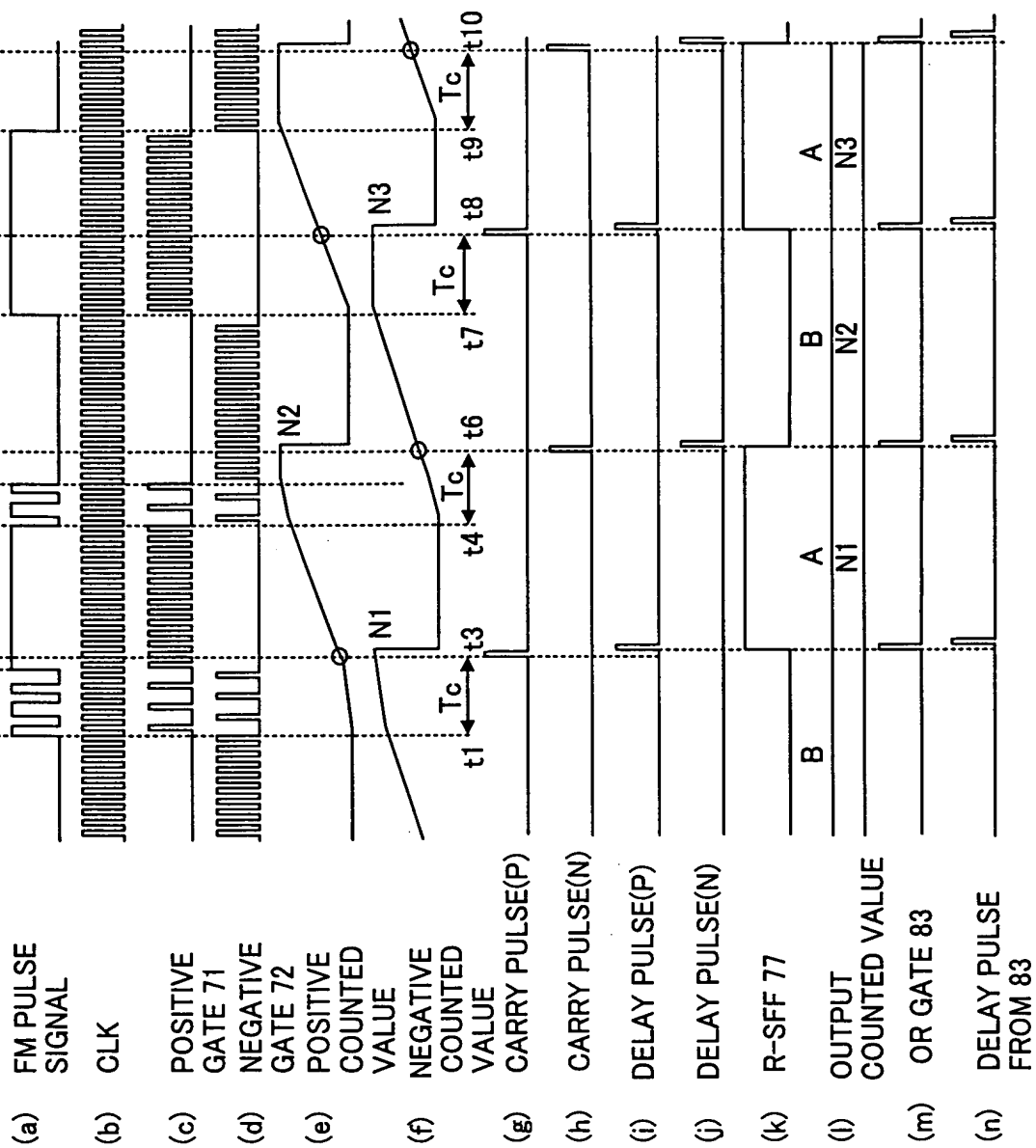
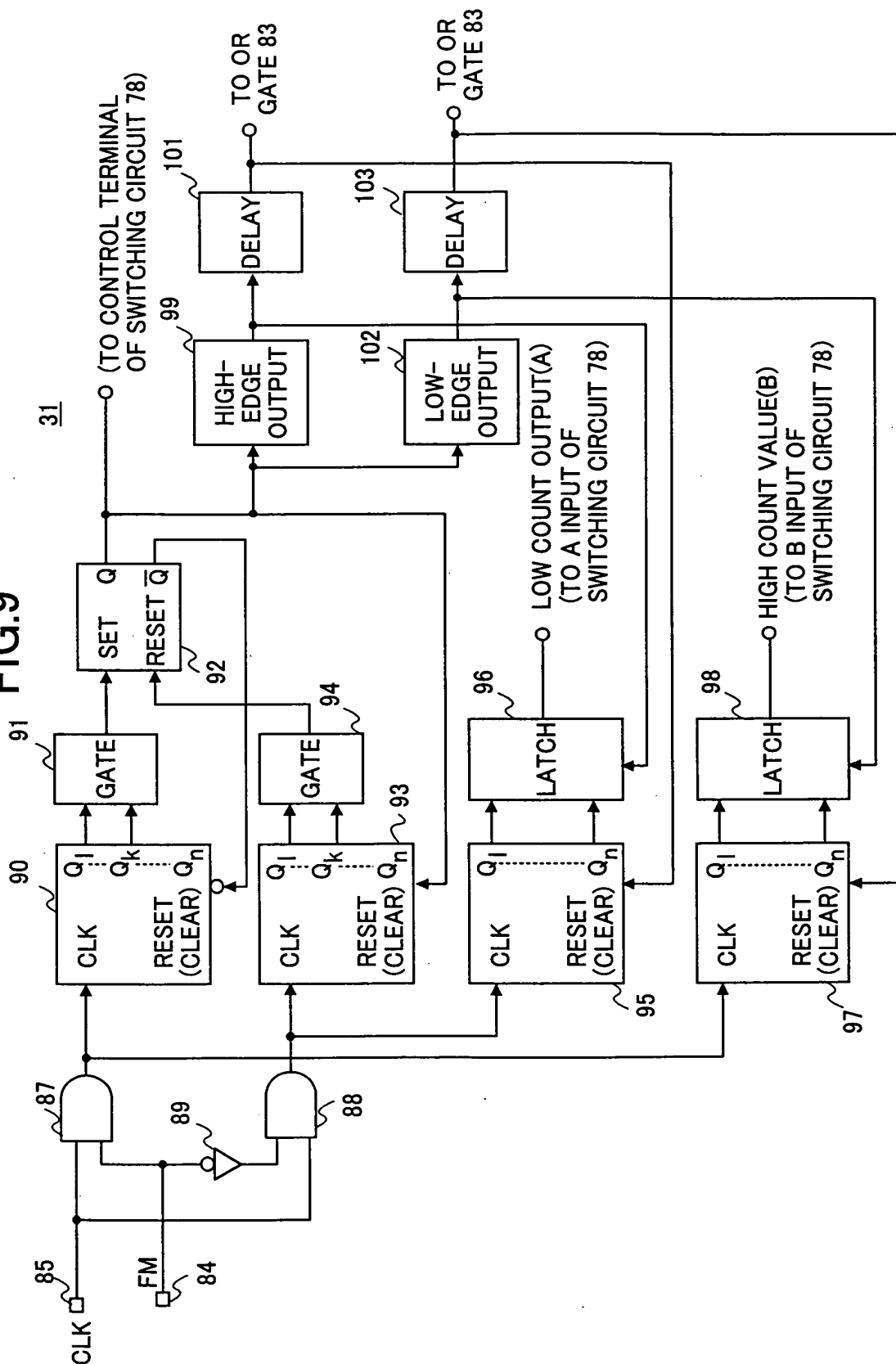


FIG.8

FIG.9



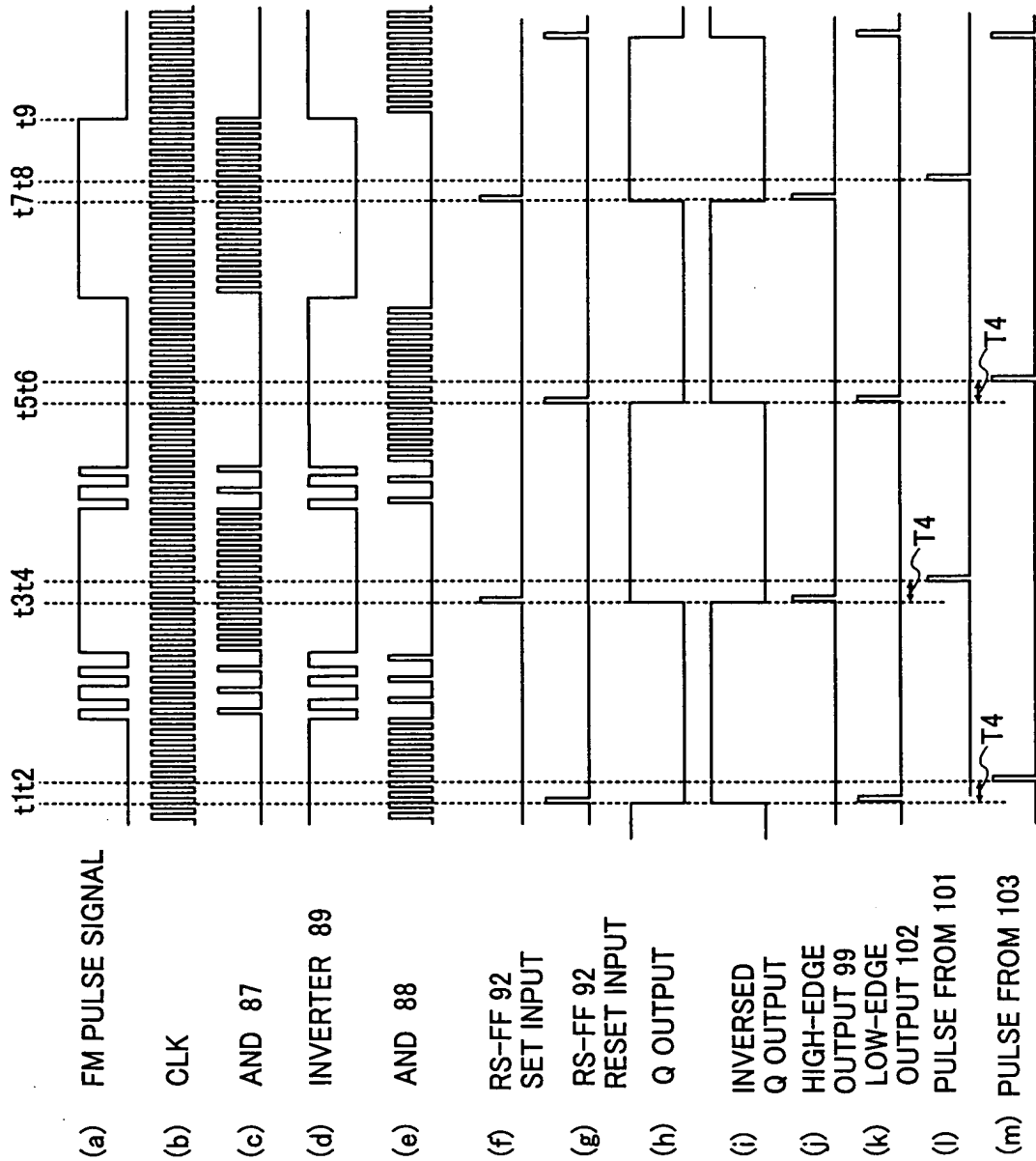


FIG.10

The diagram illustrates the internal structure of a PLL 105. It includes a 90° phase comparator 106, a VCO 107, and a frequency divider 108. The circuit also features two counters (95, 96) with latches (97, 98) and delay elements (101, 102, 103, 104). The circuit is controlled by FM 84 and CLK 85 signals.

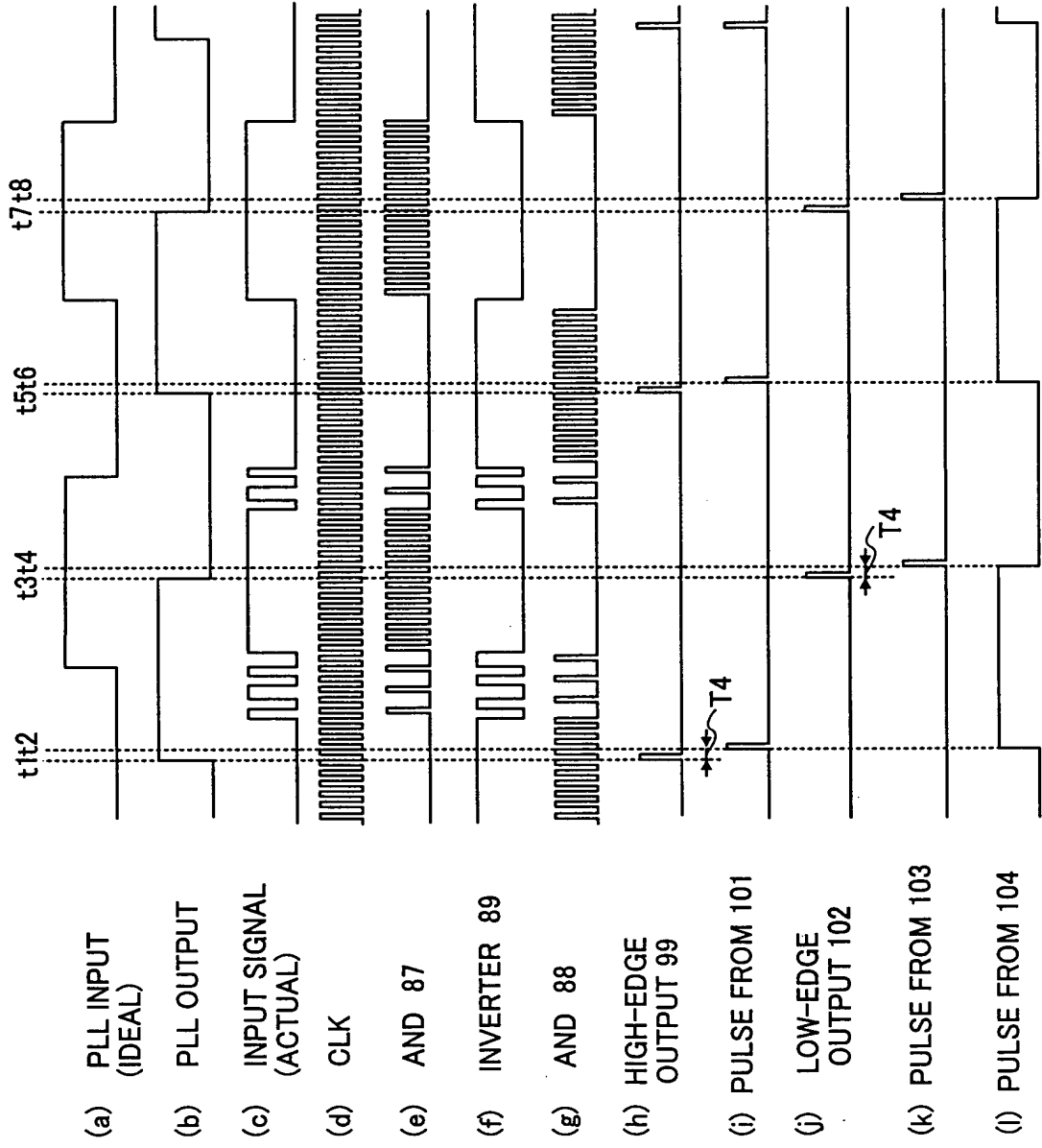


FIG.12